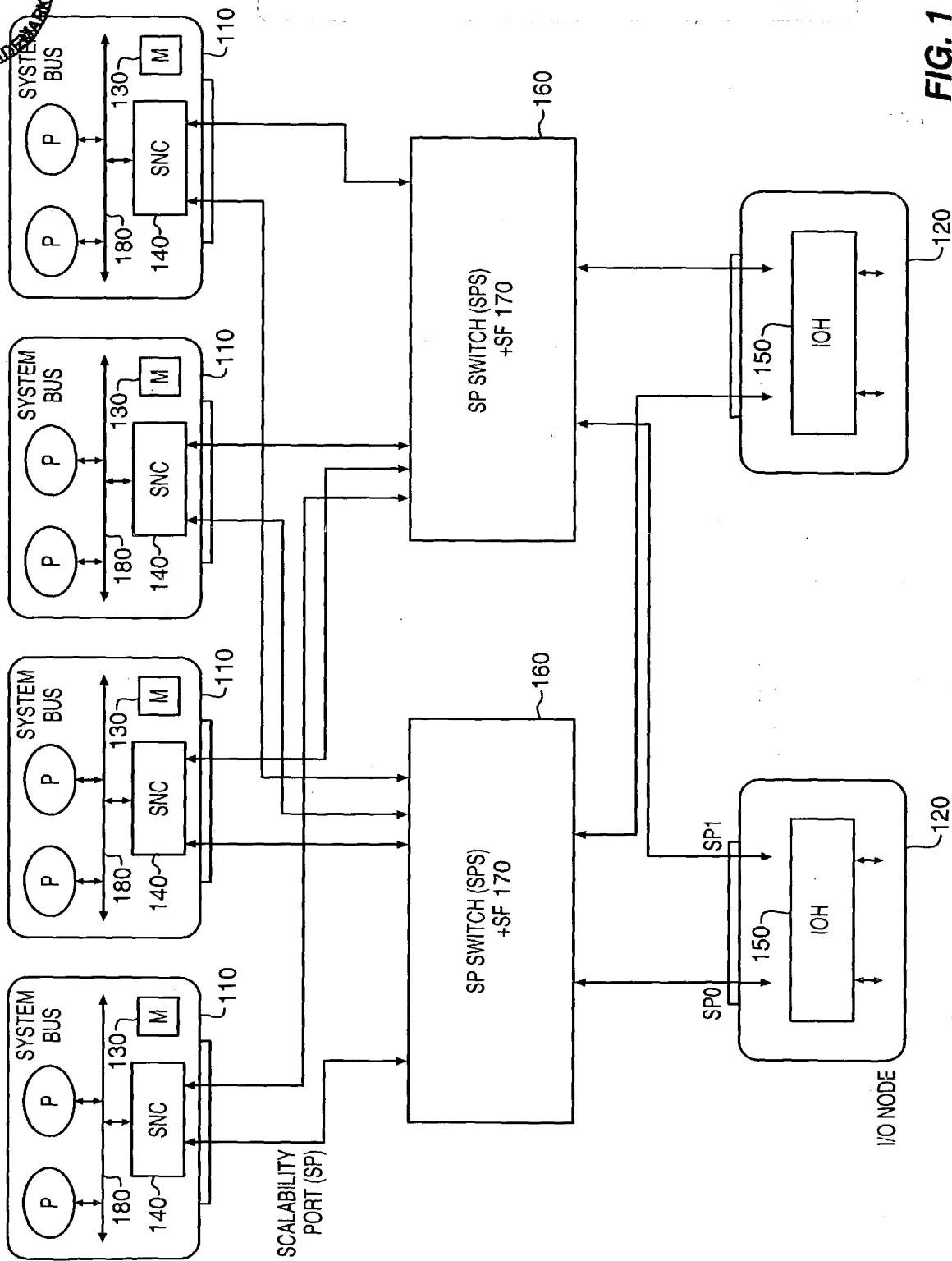


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BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP (303) 740-1980
Title: A Mechanism for Efficiently Supporting the Full MESI (Modified, Exclusive, Shared, Invalid) Protocol in Cache Coherent Multi-Node Shared Memory System
Inventor: Manoj Khare, et al.
Filing Date: December 29, 2000
Serial No.: 09/752,534
Sheet: 1 of 9

Attorney Docket No: 042390P9878

FIG. 1

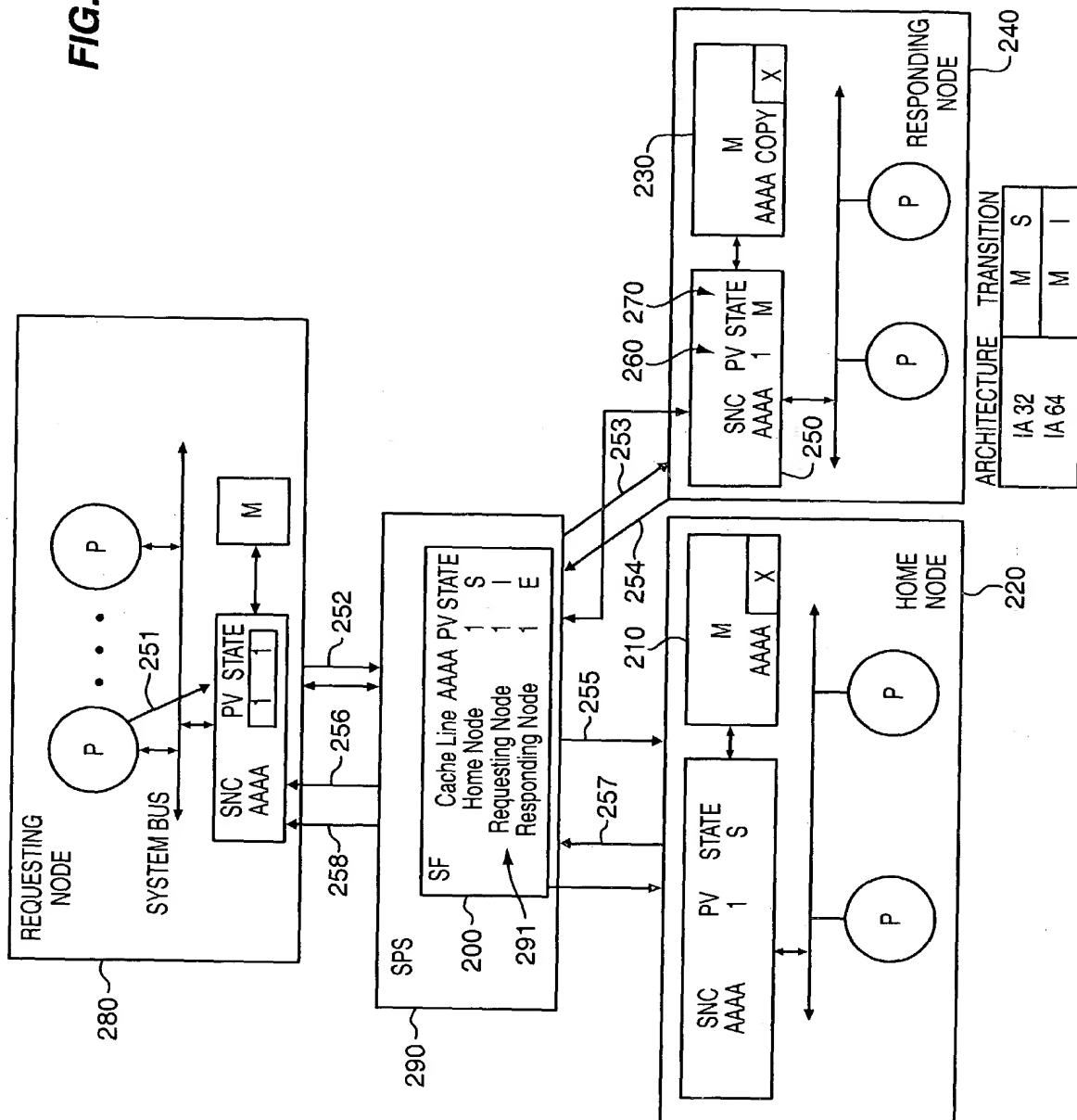




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 A Mechanism for Efficiently Supporting the Full M-S-M Modified, Exclusive, Shared, Invalid (MSI) Protocol in a Cache Collocated Multi-Node Shared Memory System
 Inventor: Manoj Khare, et al.
 Filing Date: December 29, 2000
 Serial No.: 09/752,534
 Sheet 2 of 9

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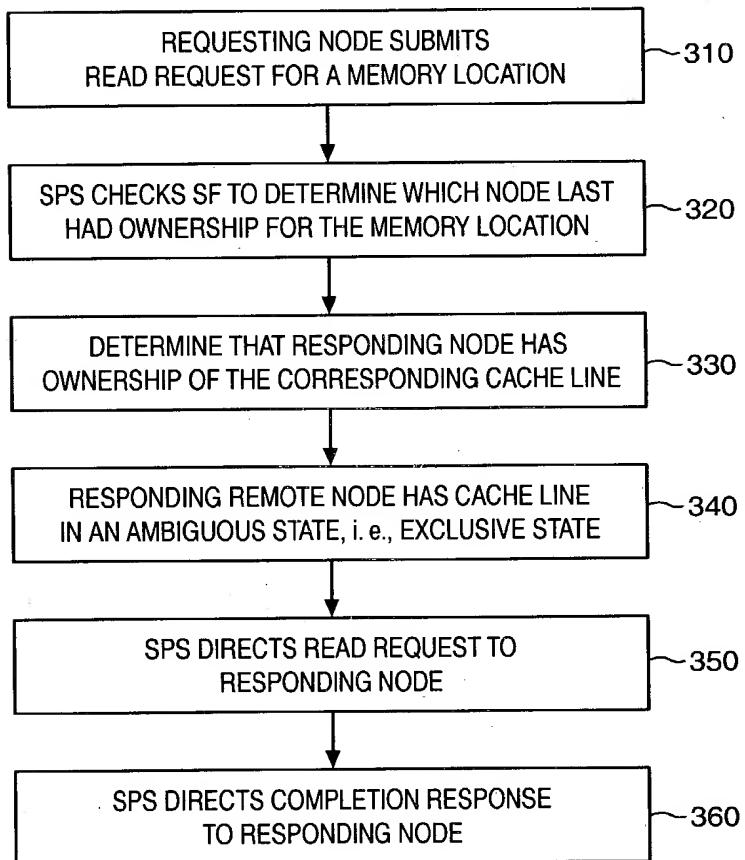
FIG. 2





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FIG. 3





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FIG. 4

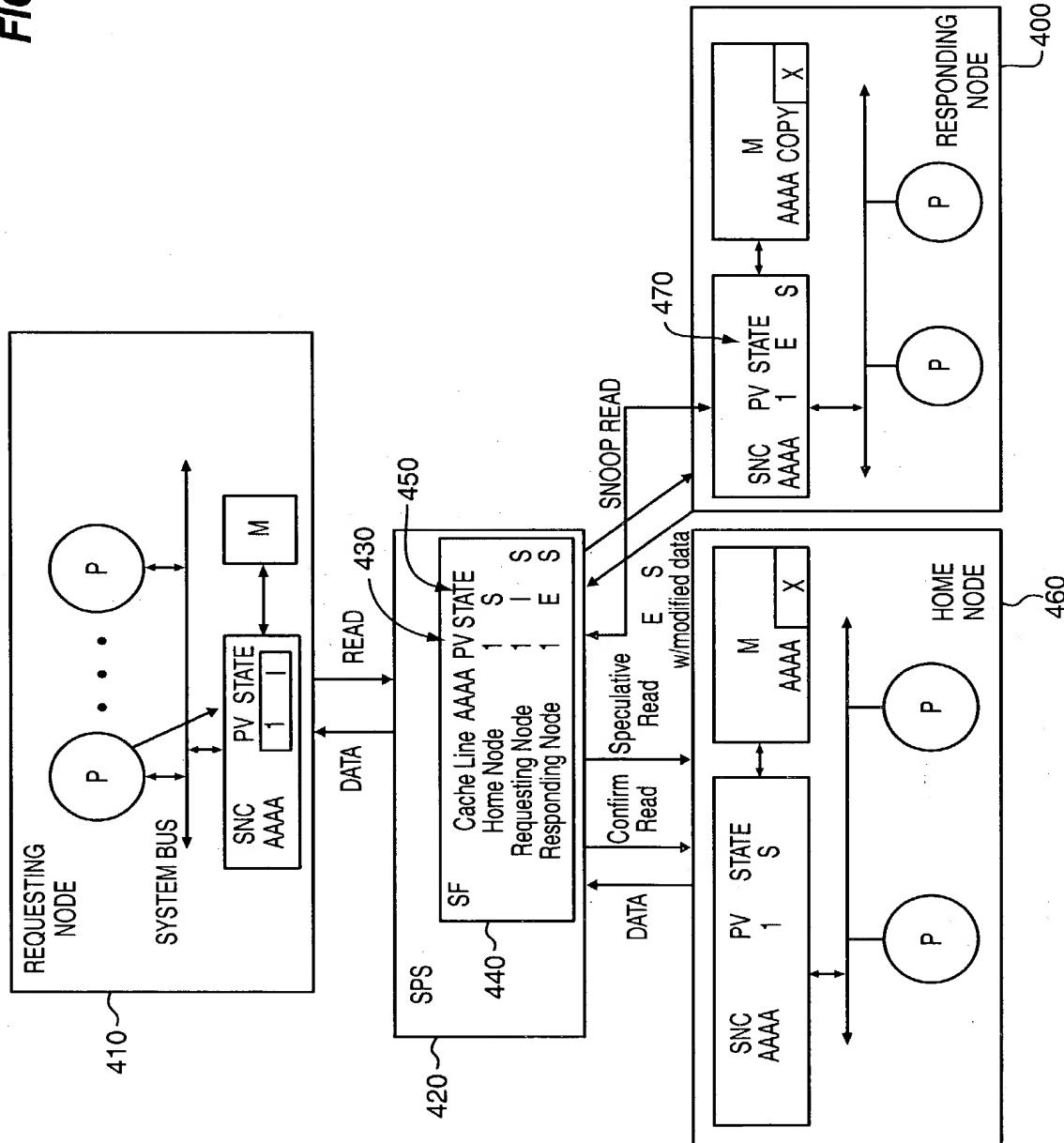
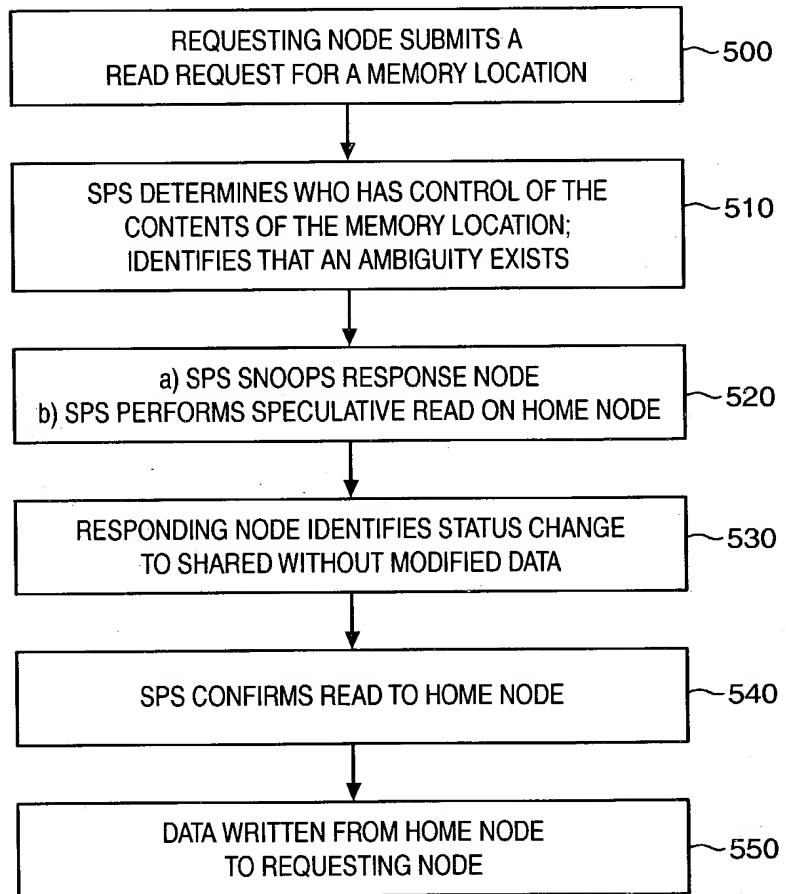




FIG. 5

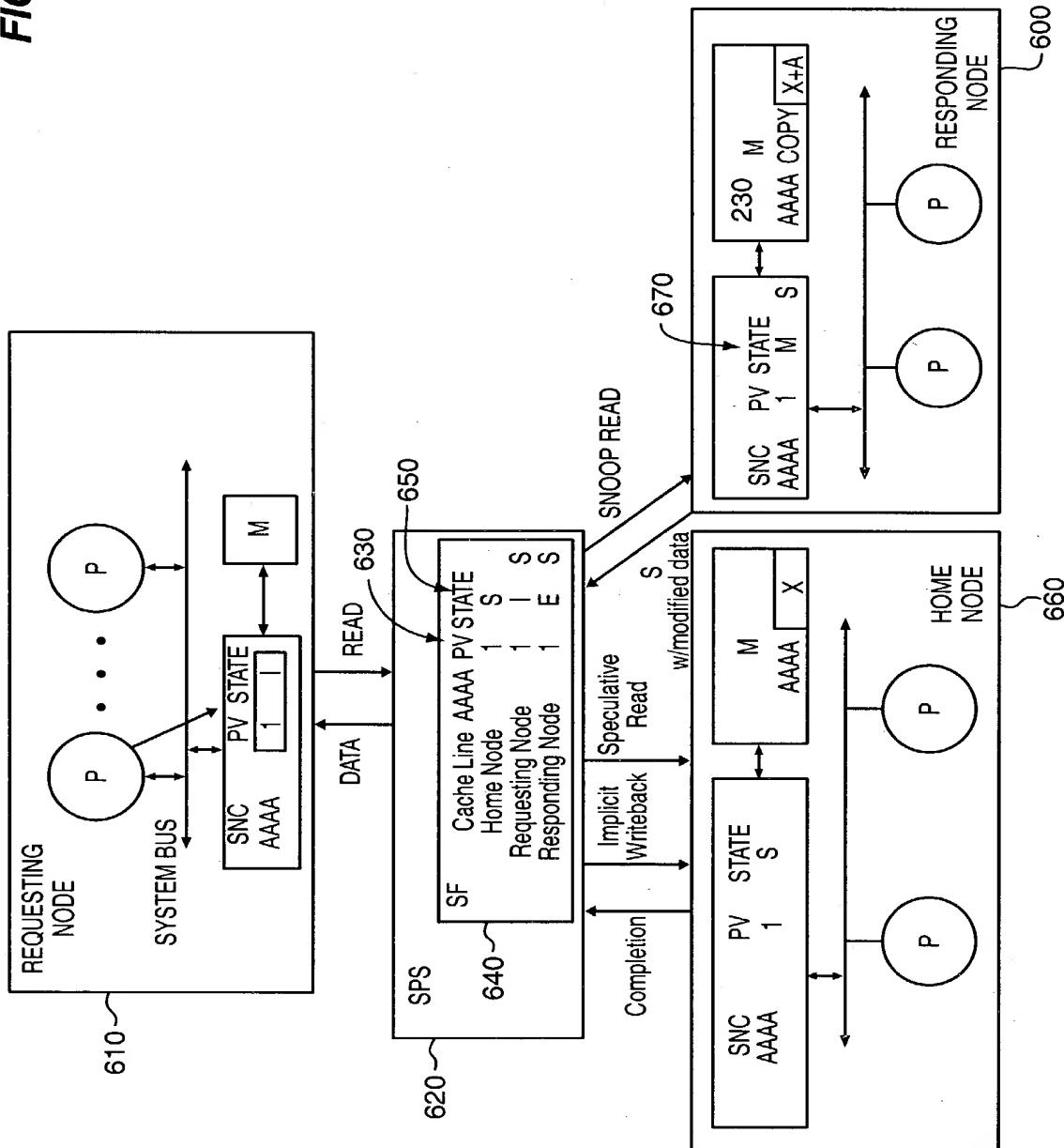




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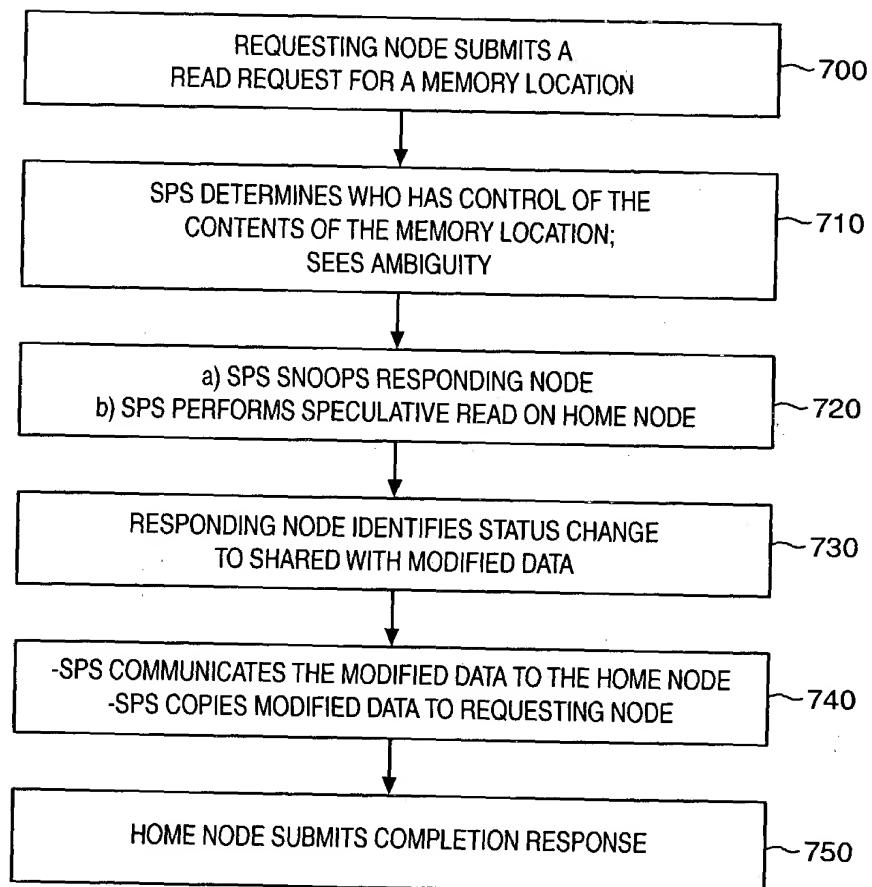
FIG. 6





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FIG. 7



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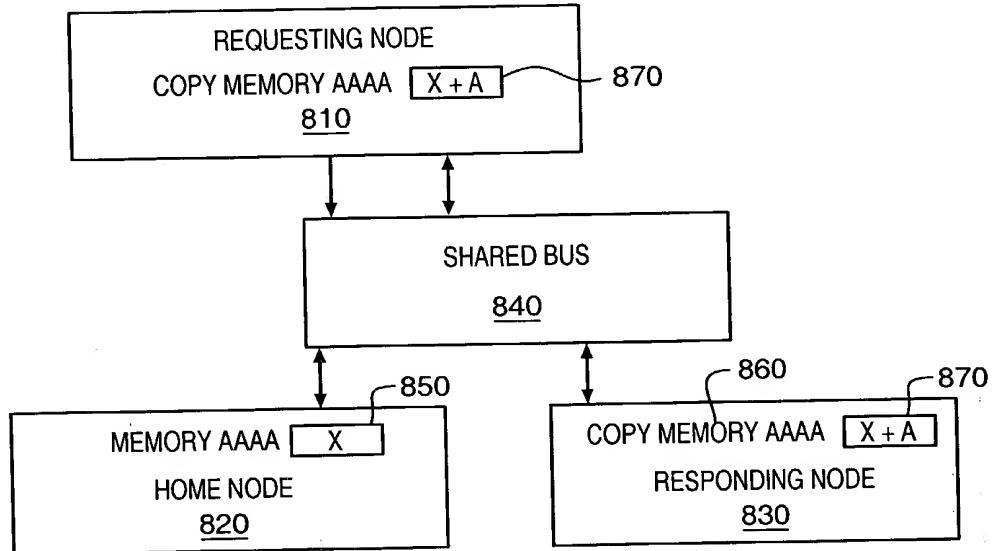


FIG. 8
PRIOR ART



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FIG. 9
PRIOR ART

